

Amendments to the Specification:

Please replace the paragraph beginning at page 50, line 16, with the following amended paragraph:

Figure 18 illustrates a block diagram of a timer configuration of a programmable digital circuit block in accordance with an embodiment of the present invention. Figure ~~[[4]]~~ 19 illustrates a block diagram of a counter configuration of a programmable digital circuit block in accordance with an embodiment of the present invention. Figure ~~[[5]]~~ 20 illustrates a block diagram of a pulse width modulator (PWM) configuration of a programmable digital circuit block in accordance with an embodiment of the present invention.